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Dated: June 5, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Hiroshi Satoh

Serial Number:

To Be Assigned

Filing Date:

Filed Herewith

Examiner/Art Group Unit:

To Be Assigned / To Be Assigned

Title:

Image Processing Device and Method

Attorney Doc. No.:

NNA-104-B

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

The references listed in the attached form PTO-1449 are cited pursuant to Rule 37 C.F.R. 1.56 to meet the duty to disclose to the Patent Office all information known to the inventor, attorney or any other person who is substantively involved in the preparation or prosecution of the application or who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application.

A copy of each cited reference is not enclosed pursuant to United States Patent and Trademark Office OG Notices: 05 August 2003 which waives the requirement to submit a copy of each cited U.S. Patent and U.S. Patent Application Publication for all U.S. National Patent Applications filed after June 30, 2003.

These references constitute all the information of which the individuals pursuant to 37 C.F.R. § 1.56(c) are currently aware.

Respectfully submitted,

YOUNG & BASILE, P.C.

Andrew R. Basile, Jr.

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MEMBERGETTETU 05 JUN 2006

PTO/SB/08A (07-05) Approved for use through 07/31/2006. OMB 0651-0031

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Sheet

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number
Filing Date
Filed Herewith
First Named Inventor
Art Unit
Examiner Name
Attorney Docket Number
To Separation 108

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Hiroshi Satoh
To Be Assigned
NNA-104-B

			U. S. PATENT D	OCUMENTS	
Examiner Initials*	Cite No.1	Number-Kind Code ^{2 (d known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		FORE	IGN PATENT DOCL	MENTS		
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T€
		WO 2004/021696	03-11-2004	Robert Bosch		
•		JP 2000-125206	04-28-2000	NEC Corp		
		EP 0 849 941 A2	06-24-1998	Raytheon Company		<u> </u>

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		NON PATENT LITERATURE DOCUMENTS	
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		NARENDRA P.M., ET AL., "Shutterless Fixed Pattern Noise Correction For Infrared Imaging Arrays", Proceedings of the SPIE, SPIE, Bellingham, VA, USA; Vol. 282, 21 April 1981, pages 44-51; XP 000991499.	

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